

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. UTILITY Patent Application

KS ^{WTO} I.P.E. SCANNED <u>GO</u> Q.A. <u>AA</u>	PATENT DATE
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APPLICATION NO. 09/940278	CONT/PRIOR	CLASS 26T E4	SUBCLASS 260	ART UNIT 2841 2821	EXAMINER PATE
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rs Stephen Ohr

APPLICANTS

3.1.2.4. *Staphylococcus aureus*

Layered circuit boards and methods of production thereof

PTO-2040
12/89[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		ISSUE BATCH NUMBER	

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